

EAST SEARCH 10/18/2007 T2M1

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	451	intermediate adj processor	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
2	BRS	L2	6	intermediate adj processor same synchroni\$5	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
3	BRS	L3	226	712/35.ccls.	USPAT
4	BRS	L4	505	712/228.ccls.	USPAT
5	BRS	L5	3146	711/147-153.ccls.	USPAT
6	BRS	L6	1025	710/5.ccls.	USPAT
7	BRS	L7	89505	MAC	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B

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8	BRS	L8	751	MAC adj unit	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
9	BRS	L9	560	MAC adj processor	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
10	BRS	L10	19	morton.in. and FPGA	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B

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11	BRS	L11	11889	input adj processor	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
12	BRS	L12	2082	L11 and (output adj processor)	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
13	BRS	L14	49	L13 and dedicated	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B

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14	BRS	L17	0	L13 and (different adj instruction adj set)	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B
15	BRS	L20	1	decoupled near2 coprocessor	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B
16	BRS	L21	1	decoupled near2 coprocessor	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B

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17	BRS	L22	1	(decoupled or asynchronous) near2 coprocessor	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
18	BRS	L23	1	(decoupled or asychr\$7) near2 coprocessor	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
19	BRS	L30	78	coprocessor with instruction with buffer	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B

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20	BRS	L36	3	synchroni\$5 with (memory adj buffer) with (main adj memory) and burst	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
21	BRS	L39	190	synchroni\$5 with (memory adj buffer)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
22	BRS	L40	0	synchroni\$5 with (memory adj buffer) with load with store	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B

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23	BRS	L41	17500	memory adj buffer	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TD B
24	BRS	L42	4709	(memory adj buffer) and load and store	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TD B
25	BRS	L15	1	L13 and (dedicated adj processor)	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B
26	BRS	L18	1	"5708830".pn.	USPAT
27	BRS	L19	1	decoupled near2 coprocessor	USPAT
28	BRS	L25	1	decoupled near coprocessor	USPAT

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29	BRS	L34	1	synchroni\$5 with (memory adj buffer) with burst	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
30	BRS	L13	68	L12 and (master adj processor)	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
31	BRS	L16	31	L13 and (instruction adj set)	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B

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32	BRS	L24	45	coprocessor near2 (instruction near2 (buffer or queue))	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
33	BRS	L26	2	"5630135".pn.	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
34	BRS	L27	2	"5790842".pn.	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B

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35	BRS	L28	2	"5781752".pn.	US- PGPUB; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B
36	BRS	L29	24	coprocessor with instruction near2 buffer	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
37	BRS	L31	24	(co adj processor) with instruction near2 buffer	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B

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38	BRS	L32	8	coprocessor with instruction with FIFO with buffer	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
39	BRS	L33	10	synchroni\$5 with (memory adj buffer) with main adj memory	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
40	BRS	L35	3	synchroni\$5 with (memory adj buffer) with (main adj memory) and burst	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B

	Type	L #	Hits	Search Text	DBs
41	BRS	L37	31	(memory adj buffer) with load with store	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
42	BRS	L38	10	synchroni\$5 with (memory adj buffer) with (main adj memory)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B
43	BRS	L43	152	(memory adj buffer) same load same store	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B